

## BIO-DATA / CV

**Name and Full Correspondence Address** [Nandini Jonnakuti]  
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**Institution** [Chaitanya Engineering College]  
**Date of Birth** [18/08/1994]  
**Gender (M/F/T)** [Female]  
**Category (Gen/SC/ST/OBC)** [General]  
**LinkedIn/ORCID/Other IDs** [Specify]

### Academic Qualifications (Undergraduate Onwards)

Degree	Year	Subject	University/Institution	%/CGPA
B.TECH	2015	ECE	JNTUK	80%
M.TECH	2021	VLSI	AU	75%

### Ph.D. Details

Institute	Guide(s)	Year of Award	Ph.D. Thesis Title
NIT ROURKELA	Dr SUMIT SAHA	pursing	2D semiconducting Materials

### Work Experience (Chronological Order)

S.No	Position	Institute	From	To
1	Asst.professor	Avanthi Research and tech	2021	2023
2	Asst.professor	Chaitanya Eng college	2023	Till now

### Publications

S.No	Authors	Title	Journal	Volume/ Page	Year
1.	Jonnakuti Nandini P S N BHASKAR	High performance of 4 Bit Mac using Reversible logic gate Design	International Journal of Advanced Technology &Engineering	Volume 2 /Issue -12	2021

### Technical Skills

Skill / Technology		
Programming	MS office ,c	Python
Software Tools	MAT LAB,Multisim	Xlinx,Cadence,Material Studio

<b>Core Skills</b>	<b>Mechine Learning</b>	<b>Deep Learning</b>
<b>Domain Knowledge</b>	<b>Analog Electronics</b>	<b>Digital Electronics,Mixed signal</b>

### FDPs & Certifications

<b>S.No</b>	<b>Program / Certification</b>	<b>Organization</b>	<b>Year</b>
1.	<b>Semiconductor Devices, Modeling &amp; Circuit Technology</b>	<b>Lendi Institute of Engineering &amp; Technology, vizianagaram</b>	<b>2024</b>
2.	<b>Recent Advancements in Communications, Data Analytics and hyper mesh using IOT Tools</b>	<b>Gayathri vidya parishad, rushikonda, Visakhapatnam</b>	<b>2024</b>
3.	<b>Semiconducting Devices</b>	<b>Gayathri vidya parishad, rushikonda, Visakhapatnam</b>	<b>2025</b>
4.	<b>VLSI Devices Circuit Design &amp; Semiconductor Materials</b>	<b>NIT Patna</b>	<b>2025</b>
5.	<b>AI and Quantum computing for advanced applications</b>	<b>MVGR College of Engineering, Vizianagaram</b>	<b>2026</b>
6.	<b>Advanced Nanomaterials and Nano Devices</b>	<b>NITRourkela</b>	<b>2026</b>

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